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6-20-01
PATENT
TESSERA 3.0-255

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of :
Warner, et al. :
Application No. : Group Art Unit:
Filed: March 19, 2001 : Examiner:
2001 : Date: March 19,
For: METHODS OF MAKING MICROELECTRONIC :
ASSEMBLIES USING COMPRESSED :
RESILIENT LAYER :
X

Jc997 U.S. PTO
09/811803
03/19/01

Box Patent Application
Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

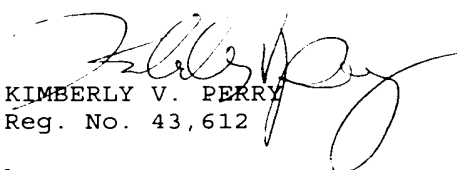
Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material. The Examiner's attention is respectfully directed to co-pending applications 08/879,922, filed June 20, 1997; 09/020,750, filed February 2, 1998; 09/195,371, filed November 18, 1998; 09/200,100, filed November 25, 1998; 09/225,669, filed January 5, 1999; 09/290,975, filed April 13, 1999; and 09/471,973, filed December 23, 1999.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Respectfully submitted,

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